

10997 U.S. PTO

10/050334



01/15/02

JCCel08A



U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10050334	01/15/2002	438	386	2813	Y. HUYNH
**APPLICANTS: Agarwal Vishnu; Mercaldi Garry;					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A DIV OF 09/653,156 08/31/2000					
** FOREIGN APPLICATIONS VERIFIED:					
PUB. OR NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		35 USC 110 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiners's initials <i>yn</i>				MI22-1913	
TITLE : Capacitor fabrication methods and capacitor constructions					
U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G. <input checked="" type="checkbox"/>
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Shoots Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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